

Abstracts

A Broadband, Planar, Monolithic Resistive Frequency Doubler (1994 [MCS])

S.A. Maas and Y. Ryu. "A Broadband, Planar, Monolithic Resistive Frequency Doubler (1994 [MCS])." 1994 Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 94.1 (1994 [MCS]): 175-178.

This paper describes a resistive Schottky-diode frequency doubler having an untuned output frequency range of 16 to 40 GHz. The multiplier uses a unique planar structure to achieve 12 dB average conversion loss, 2 dBm output power, and 20 dBC fundamental-frequency rejection.

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